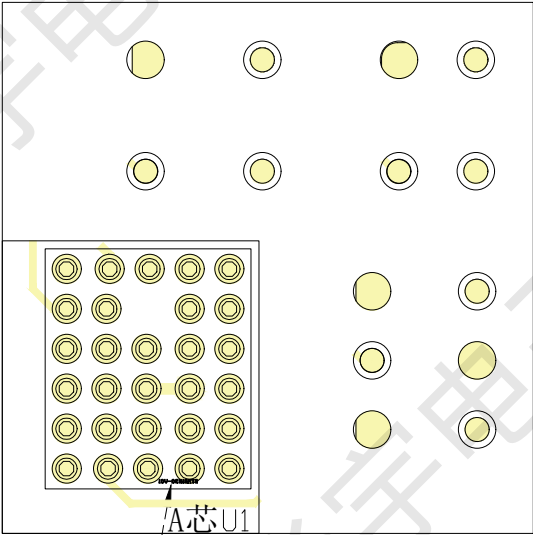


 池州华宇电子科技股份有限公司 CHI ZHOU HISEMI ELECTRONICS TECHNOLOGY CO.,LTD 焊线图纸 Bonding Diagram	客户代码 Customer No.		1200	线图号 Drawing No.		HY-PX-1200-058 A		页码 Page 1 / 3
	产品名称 Product Type		STR21230-51 M1.0		封装外型 PKG Type		FCLGA24L(2X2X0.65-P0.33)	
助焊剂 Flux	/	/	/	/	/	塑封料型号(绿色环保) Compound Type (Green)		基板编码 Substrate No.
TAC026S	/	/	/	/	/	首选(Preferred): / 备选(Optional):		S23051JL1M
客户图号 Customer drawing NO.								

样品验证，禁止量产！

PIN1

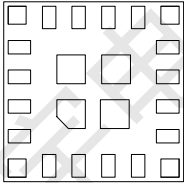


A芯U1

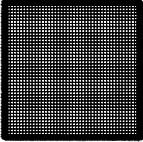
STR21230-V0

注意：

- 1.此图纸只做SMT工序，贴U1 die;
- 2.本次手动上料，客户未提供料盒;
- 2.后切割道宽度0.25mm。



底部示意图

框架传送方向（装片）： L/F Direction (D/A)：				实物图： Chip photo：				特殊说明 Special Instructions：			
 按单颗显示方向进料								SMT注意： 1.控制溢锡，防止沾锡。			

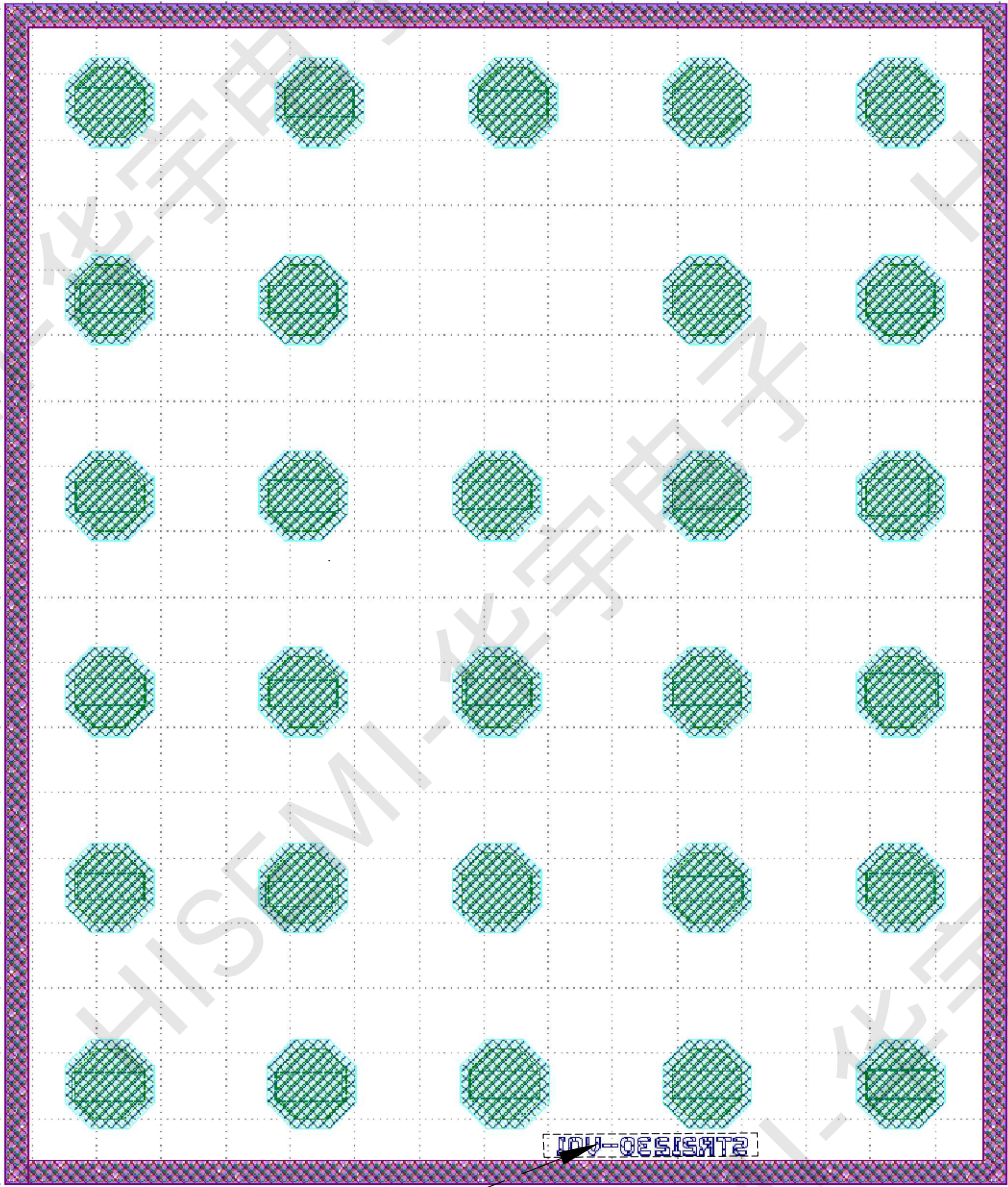
序号	芯片名称	Die ID	芯片尺寸 (with scribe line)	划片道 宽度	固片工艺	芯片厚度 UM	来料方式	Wafer material*	Bump type*	Bump composition*	Min bump pitch (space + bump size)* UM	Min bump height* UM	Min bump diameter* UM
A芯 U1	WUM230SD2E-V01	STR21230-V01	837.8*966.5(μm²) 32.98*38.05mil²)	62um	NA	200um (不包含 bump高度)	卷带	SOI	Copper Pillar Bump	35cu+3NI+30Sn	150	68	80
拟制 Prepared by		制图日期 Create Date			2025/10/18			生效日期 Effective Date		客户确认签字/盖章: Customer Signature			
研发审核 R&d check		产品工程审核 Product engineering check			批准 Approved by								

*温馨提示：图纸为产品下线生产的唯一依据，请您认真确认，我司依据您回签后的图纸生产，如图纸错误会产生不可估量损失，谢谢！

*warm tips: the drawing is the only basis for the production of the product. Please confirm it carefully. Our company will produce the drawings according to the drawings you have signed back, such as drawing mistakes, which will produce inestimable loss. Thank you

U1 ball map图 (TOP view)

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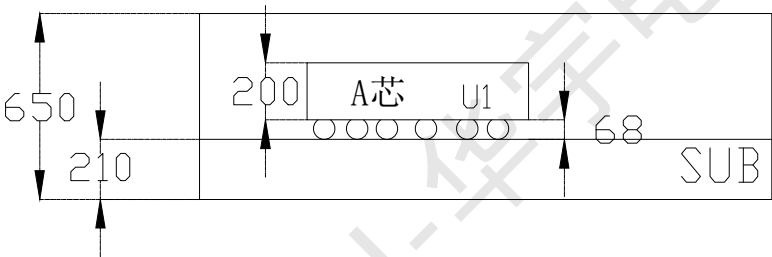
按目标区域取DIE：
ID：STR21230-V01

拟制 Prepared by		制图日期 Create Date	2025/10/18	生效日期 Effective Date		客户确认签字/盖章： Customer Signature
研发审核 R&d check		产品工程审核 Product engineering check		批准 Approved by		

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	产品名称 Product Type	STR21230-51 M1.0		封装外型 PKG Type	FCLGA24L(2X2X0.65-P0.33)	

样品验证，禁止量产！



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